EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	55	"712".clas. logical near1 register$1 near5 (queue or buffer)	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/06/27 11:40
L2	55	L1	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/06/27 11:40
L3	80	logical near1 register$1 near5 (queue or buffer)	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/06/27 11:40
L4	80	L3	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/06/27 11:40
S1	8024	checkpoint$4 or (renam$4 near5 recover$3 with (predict$4 or speculat$4)) register$1 near5 (queue or buffer) same pointer$1	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/01/04 14:08
S2	298	"712".clas. checkpoint$4 or (renam$4 near5 recover$3 with (predict$4 or speculat$4)) register$1 near5 (queue or buffer) same pointer$1	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/01/04 14:08
S3	298	"712".clas. checkpoint$4 or (renam$4 near5 recover$3 with (predict$4 or speculat$4)) register$1 near2 (queue or buffer) with pointer$1	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/01/04 14:08
S4	4	"712".clas. checkpoint$4 register$1 near2 (queue or buffer) with pointer$1	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/01/04 14:08
S5	22	"712".clas. checkpoint$4 register$1 near5 (queue or buffer) same pointer$1	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/01/04 14:10
S6	20	"712".clas. checkpoint$4 register$1 near5 (queue or buffer) same pointer$1 (branch or exception$1)	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/01/04 14:10
S7	16	"712".clas. checkpoint$4 register$1 near5 (queue or buffer) same pointer$1 (branch and exception$1)	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/01/04 14:12

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S8	6	"712".clas. checkpoint$4 register$1 near2 (queue or buffer) same pointer$1 (branch and exception$1)	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/01/04 14:15
S9	28	"712".clas. checkpoint$4 (register$1 or pointer$1) near2 (queue or buffer) same pointer$1 (branch and exception$1)	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/01/04 15:15
S10	76	logical near1 register$1 near5 (queue or buffer)	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/01/04 14:21
S11	53	"712".clas. logical near1 register$1 near5 (queue or buffer)	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/01/04 14:27
S12	3	"712".clas. logical near1 register$1 near5 (queue or buffer) same circular	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/01/04 14:22
S13	4	"712".clas. logical near1 register$1 near5 (queue or buffer) checkpoint$4	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/01/04 14:31
S14	2	"712".clas. logical near1 register$1 near2 (queue or buffer) checkpoint$4	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/01/04 14:32
S15	6	"712".clas. logical near1 register$1 near2 queue	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/01/04 15:01
S16	6	"712".clas. "logical register" near2 queue	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/01/04 15:01
S17	0	"712".clas. "logical registers" near2 queue	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/01/04 15:01
S18	37	checkpoint$4 same branch same exception "712".clas.	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/01/05 17:02
S19	25	checkpoint$4 with branch with exception "712".clas.	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/01/05 17:02

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S20	26	checkpoint$4 with branch with exception "712".clas.	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/06/26 18:02
S21	26	S20	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/06/26 18:02
S22	55	"712".clas. logical near1 register$1 near5 (queue or buffer)	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/06/26 18:02
S23	55	S22	US-PGPUB; USPAT; USOCR; DERWENT	AND	OFF	2006/06/27 11:40